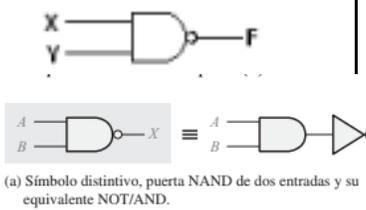
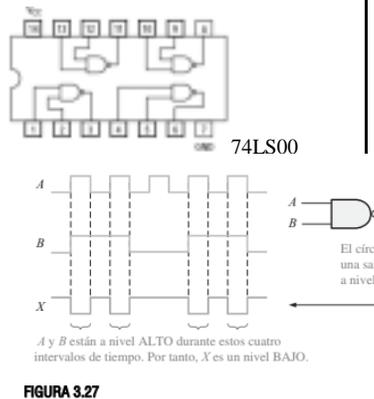
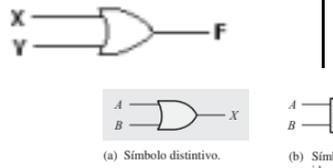
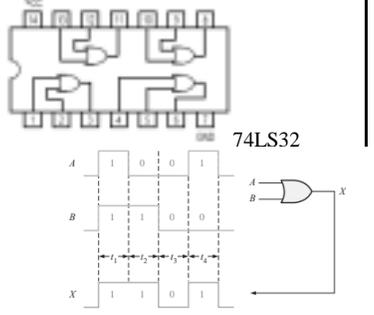
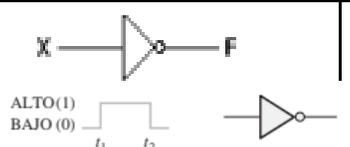
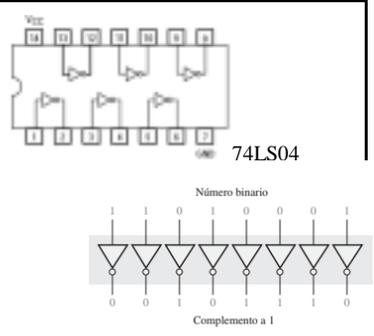
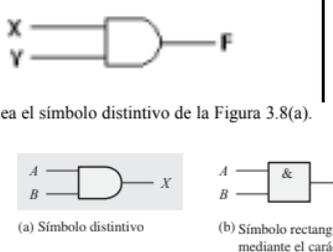
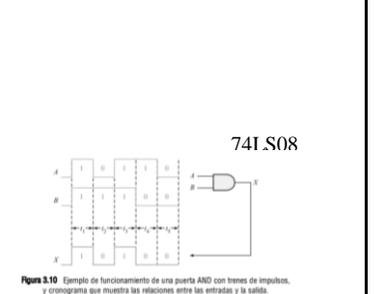


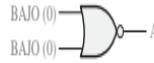
1.- Completa la siguiente tabla con los datos de las Compuertas lógicas:

NOMBRE	SÍMBOLO LÓGICO	FUNCIÓN ALGEBRAICA	TABLA DE VERDAD	CIRCUITO INTEGRADO																																													
NAND (Capítulo 3, página 141)	 <p>(a) Símbolo distintivo, puerta NAND de dos entradas y su equivalente NOT/AND.</p>	$F = \overline{XY}$ BAJO (0) ALTO (1)	$\underline{X \ Y \ F}$ <table border="1"> <tr><th>Entradas</th><th>A</th><th>B</th><th>F</th></tr> <tr><td>0</td><td>0</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>0</td><td>1</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>1</td><td>0</td></tr> <tr><td>1</td><td>0</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td><td>0</td></tr> <tr><td>1</td><td>1</td><td>0</td><td>0</td></tr> <tr><td>1</td><td>1</td><td>1</td><td>0</td></tr> </table>	Entradas	A	B	F	0	0	0	1	0	0	1	1	0	1	0	1	0	1	1	0	1	0	0	1	1	0	1	0	1	1	0	0	1	1	1	0	 <p>74LS00</p> <p>FIGURA 3.27</p>									
Entradas	A	B	F																																														
0	0	0	1																																														
0	0	1	1																																														
0	1	0	1																																														
0	1	1	0																																														
1	0	0	1																																														
1	0	1	0																																														
1	1	0	0																																														
1	1	1	0																																														
OR (Capítulo 3, página 135)	 <p>(a) Símbolo distintivo. (b) Símbolo idéntico.</p>	$F = X + Y$ BAJO (0) ALTO (1)	$\underline{X \ Y \ F}$ <table border="1"> <tr><th>Entradas</th><th>A</th><th>B</th><th>F</th></tr> <tr><td>0</td><td>0</td><td>0</td><td>0</td></tr> <tr><td>0</td><td>0</td><td>1</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>0</td><td>1</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>1</td><td>1</td></tr> </table>	Entradas	A	B	F	0	0	0	0	0	0	1	1	0	1	0	1	0	1	1	1	1	0	0	1	1	0	1	1	1	1	0	1	1	1	1	1	 <p>74LS32</p> <p>FIGURA 3.19</p>									
Entradas	A	B	F																																														
0	0	0	0																																														
0	0	1	1																																														
0	1	0	1																																														
0	1	1	1																																														
1	0	0	1																																														
1	0	1	1																																														
1	1	0	1																																														
1	1	1	1																																														
INVERSOR (Capítulo 3, página 125)	 <p>ALTO (1) BAJO (0)</p>	$F = \overline{X}$ Entu	$\underline{X \ F}$ <table border="1"> <tr><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td></tr> </table>	0	1	1	0	 <p>74LS04</p> <p>FIGURA 3.7</p>																																									
0	1																																																
1	0																																																
AND (Capítulo 3, página 129)	 <p>(a) Símbolo distintivo (b) Símbolo rectángulo mediante el carácter &</p>	$F = XY$ BAJO (0) ALTO (1)	$\underline{X \ Y \ F}$ <table border="1"> <tr><th>Entradas</th><th>A</th><th>B</th><th>Salida</th><th>X</th></tr> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr> <tr><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr> <tr><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td></tr> <tr><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td></tr> <tr><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr> </table>	Entradas	A	B	Salida	X	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1	1	1	1	1	0	0	0	0	1	0	1	0	0	1	1	0	0	0	1	1	1	1	1	 <p>74LS08</p> <p>FIGURA 3.10</p>
Entradas	A	B	Salida	X																																													
0	0	0	0	0																																													
0	0	1	0	0																																													
0	1	0	0	0																																													
0	1	1	1	1																																													
1	0	0	0	0																																													
1	0	1	0	0																																													
1	1	0	0	0																																													
1	1	1	1	1																																													

NOR
(Capítulo 3, página 146)



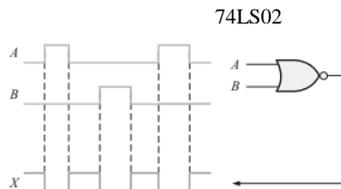
$F = \overline{(X + Y)}$

BAJO (0)  A

ALTO (1)  B

Entradas		Salida
A	B	X
0	0	1
0	1	0
1	0	0
1	1	0

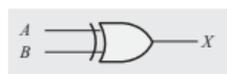
1 = ALTO, 0 = BAJO



74LS02

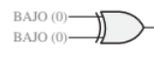
NOMBRE	SÍMBOLO LÓGICO	FUNCIÓN ALGEBRAICA	TABLA DE VERDAD	CIRCUITO INTEGRADO
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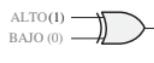
XOR
(Capítulo 3, página 152)



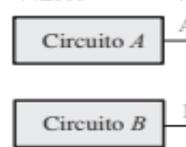
(a) Símbolo distintivo

1.

BAJO (0)  E

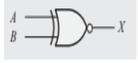
ALTO (1)  B

Entradas		Salida
A	B	X
0	0	0
0	1	1
1	0	1
1	1	0

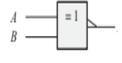


74LS86

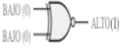
XNOR
(Capítulo 3, página 153)



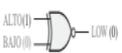
(a) Símbolo distintivo



(b) Símbolo rectangular

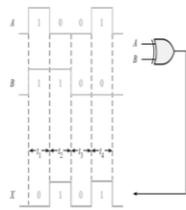
BAJO (0)  ALTO (1)

BAJO (0)  ALTO (1)

ALTO (1)  LOW (0)

ALTO (1)  ALTO (1)

Entradas		Salida
A	B	X
0	0	1
0	1	0
1	0	0
1	1	1



74LS266

FIGURA 3.46 Ejemplo de funcionamiento de la puerta OR-exclusiva con tres